* Introduction page (500)

- Project planning and organisation (1000)

1. Gantt chart

2. Communication methods, call log

* ISA design (1000) \*pedro

- Design decisions (1000)

1. stack

2. status reg

3. number of reg

4. dual port ram (Harvard arch)

5. pipelining

* MULT methods (1000) \*geri
* Each of the three algorithms (1500 total) \*500 each
* Design & implementation (1000)
  + REG FILE
  + ALU
  + DECODER

- Simulation/results (1000): 300 words on each algorithm

- Assembler (500)

* Future developments (500)